

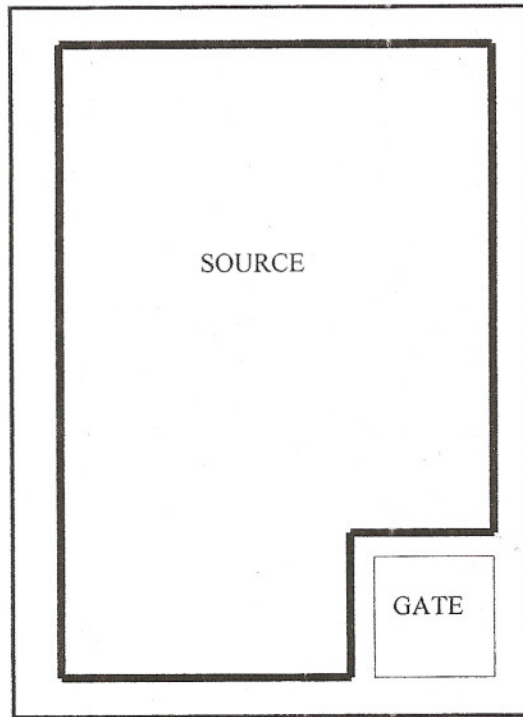


# Sierra Components, Inc.

2222 Park Place Building 3 Suite E • Minden, Nevada 89423

Phone: 775.783.4940 Fax: 775.783.4947

Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



**Topside Metal: Aluminum**  
**Backside Metal: Cr/Ni/Ag**  
**Backside Potential: Drain**  
**Bond Pad Size: .004" min.**  
**Mask Ref:**

**APPROVED BY: C Dawson**

**DIE SIZE : .041" X .070"**

**DATE: 5/18/06**

**MFG: International Rectifier**

**THICKNESS: .013"**

**Part # IRLC014N**